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WELSH & KATZ, LTD
WALTER J. KAWULA, JR
120 SOUTH RIVERSIDE PLAZA
22 ND FLOOR
CHICAGO, IL 60606-3912

EXAMINER

TORRES, JUAN A

ART UNIT	PAPER NUMBER
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2631

DATE MAILED: 11/21/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No.	Applicant(s)	
	10/021,220	FRIGON, JEAN-FRANCOIS	
	Examiner	Art Unit	
	Juan A. Torres	2631	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 11 December 2001.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-24 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-24 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☒ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 11 December 2001 is/are: a) ☐ accepted or b) ☒ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413) |
| 2) <input type="checkbox"/> Notice of Draftperson's Patent Drawing Review (PTO-948) | Paper No(s)/Mail Date: _____ |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| Paper No(s)/Mail Date <u>04-15-03</u> | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

Drawings

The drawings are objected to as failing to comply with 37 CFR 1.84(p)(5) because they include the following reference character(s) not mentioned in the description: "1", "3" and "5" from figure 1; "45" from figure 4; "65" from figure 5; "115" of figure 9; "141" of figure 12; "191" in figure 17. Corrected drawing sheets in compliance with 37 CFR 1.121(d), or amendment to the specification to add the reference character(s) in the description in compliance with 37 CFR 1.121(b) are required in reply to the Office action to avoid abandonment of the application. Any amended replacement drawing sheet should include all of the figures appearing on the immediate prior version of the sheet, even if only one figure is being amended. Each drawing sheet submitted after the filing date of an application must be labeled in the top margin as either "Replacement Sheet" or "New Sheet" pursuant to 37 CFR 1.121(d). If the changes are not accepted by the examiner, the applicant will be notified and informed of any required corrective action in the next Office action. The objection to the drawings will not be held in abeyance.

The drawings are objected to as failing to comply with 37 CFR 1.84(p)(5) because they do not include the following reference sign(s) mentioned in the description: "23" in figure 4 (page 5 line11); "145" in figure 12 (see page 21 line 20);. Corrected drawing sheets in compliance with 37 CFR 1.121(d) are required in reply to the Office action to avoid abandonment of the application. Any amended replacement drawing sheet should include all of the figures appearing on the immediate prior version

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of the sheet, even if only one figure is being amended. Each drawing sheet submitted after the filing date of an application must be labeled in the top margin as either "Replacement Sheet" or "New Sheet" pursuant to 37 CFR 1.121(d). If the changes are not accepted by the examiner, the applicant will be notified and informed of any required corrective action in the next Office action. The objection to the drawings will not be held in abeyance.

The drawings are objected to as failing to comply with 37 CFR 1.84(p)(4) because reference character "67" has been used to designate both high level schematic/logic receiver and M parallel diversity branches in figures 6 and 7. Corrected drawing sheets in compliance with 37 CFR 1.121(d) are required in reply to the Office action to avoid abandonment of the application. Any amended replacement drawing sheet should include all of the figures appearing on the immediate prior version of the sheet, even if only one figure is being amended. Each drawing sheet submitted after the filing date of an application must be labeled in the top margin as either "Replacement Sheet" or "New Sheet" pursuant to 37 CFR 1.121(d). If the changes are not accepted by the examiner, the applicant will be notified and informed of any required corrective action in the next Office action. The objection to the drawings will not be held in abeyance.

The drawings are objected to as failing to comply with 37 CFR 1.84(p)(4) because reference characters "137" and "73" have both been used to designate "Receive and Filter signal" (see figures 11 and 12 page 32 lines 10-14). Corrected drawing sheets in compliance with 37 CFR 1.121(d) are required in reply to the Office

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action to avoid abandonment of the application. Any amended replacement drawing sheet should include all of the figures appearing on the immediate prior version of the sheet, even if only one figure is being amended. Each drawing sheet submitted after the filing date of an application must be labeled in the top margin as either "Replacement Sheet" or "New Sheet" pursuant to 37 CFR 1.121(d). If the changes are not accepted by the examiner, the applicant will be notified and informed of any required corrective action in the next Office action. The objection to the drawings will not be held in abeyance.

Figures 1-7 and 21 should be designated by a legend such as --Prior Art-- because only that which is old is illustrated. See MPEP § 608.02(g). Corrected drawings in compliance with 37 CFR 1.121(d) are required in reply to the Office action to avoid abandonment of the application. The replacement sheet(s) should be labeled "Replacement Sheet" in the page header (as per 37 CFR 1.84(c)) so as not to obstruct any portion of the drawing figures. If the changes are not accepted by the examiner, the applicant will be notified and informed of any required corrective action in the next Office action. The objection to the drawings will not be held in abeyance.

The informal drawings are not of sufficient quality to permit examination. Accordingly, replacement drawing sheets in compliance with 37 CFR 1.121(d) are required in reply to this Office action. The replacement sheet(s) should be labeled "Replacement Sheet" in the page header (as per 37 CFR 1.84(c)) so as not to obstruct any portion of the drawing figures. If the changes are not accepted by the examiner, the

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applicant will be notified and informed of any required corrective action in the next Office action.

Applicant is given a TWO MONTH time period to submit new drawings in compliance with 37 CFR 1.81. Extensions of time may be obtained under the provisions of 37 CFR 1.136(a). Failure to timely submit replacement drawing sheets will result in ABANDONMENT of the application.

New corrected drawings in compliance with 37 CFR 1.121(d) are required in this application because the informal drawings are not of sufficient quality to permit examination and the considerable number of objections. Applicant is advised to employ the services of a competent patent draftsman outside the Office, as the U.S. Patent and Trademark Office no longer prepares new drawings. The corrected drawings are required in reply to the Office action to avoid abandonment of the application. The requirement for corrected drawings will not be held in abeyance.

Specification

The disclosure is objected to because of at least the following informalities:

a) Line 2 of page 4 the recitation "Mulitple" is improper; it is suggested to be changed to "Multiple".

b) Line 19 of page 8 the recitation "consequitive" is improper; it is suggested to be changed to "consecutive".

c) Line 9 of page 10 the recitation "55" is improper; it is suggested to be changed to "54" (see figure 5).

d) Line 11 of page 10 the recitation "57" is improper; it is suggested to be changed to "55" (see figure 5).

e) Line 15 of page 10 the recitation "60" is improper; it is suggested to be changed to "61" (see figure 5).

f) Line 22 of page 11 the recitation "65" is improper; it is suggested to be changed to "67" (see figure 6).

g) Line 5 of page 22, the recitation "...has bee achieved." This is improper; it is suggested to be changed to "...has been achieved.".

h) Line 1 of page 23 the recitation "153" is improper; it is suggested to be changed to "155" (see figure 13).

i) Line 15 of page 24 the recitation "correslation" is improper; it is suggested to be changed to "correlation".

j) Line 21 of page 24 the recitation "161" is improper; it is suggested to be changed to "65" (see figure 15) (165 is not possible because in figure 13 165 means something different; 161 is not possible because in figure 13 161 means something different).

k) Line 3 of page 25 the recitation "165" is improper; it is suggested to be changed to "65" (see figure 15) (165 is not possible because in figure 13 165 means something different; 161 is not possible because in figure 13 161 means something different).

l) Line 5 of page 26 the recitation "181" is improper; it is suggested to be changed to "183" (see figure 16).

m) Line 7 of page 26 the recitation "183" is improper; it is suggested to be changed to "171" (see figure 16).

Appropriate correction is required.

The lengthy specification has not been checked to the extent necessary to determine the presence of all possible minor errors. Applicant's cooperation is requested in correcting any errors of which applicant may become aware in the specification.

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

Claims 1-24 are rejected under 35 U.S.C. 103(a) as being unpatentable over admitted prior art.

As per claims 1 and 12, admitted prior art discloses a method for synchronizing a mobile terminal to a wireless network comprising receiving a signal comprising a synchronization code broadcast by a base-station with a mobile terminal (figure 1 and figure 2 block 1 page 3 lines 4-22); processing said received signal to determine at least one output value (figure 2 block 13 page 3 lines 4-22); processing the output value to determine said synchronization code (figure 2 blocks 15, 19, 21 and 17 page 3 lines 4-22); processing said received signal to synchronize a local oscillator of said mobile terminal based on said synchronization code (page 8 lines 11-15). Admitted prior art

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also discloses receiving signal comprises at least two diversity branches and at least two filters and processing the data in at least two diversity branches (figures 6 and 7 block 67 page 11 line 1 to page 12 line 20). Admitted prior art concerning code synchronization and admitted prior art concerning diversity are analogous art because they are from the same field of endeavor. At the time of the invention, it would have been obvious to a person of ordinary skill in the art to incorporate in the code synchronization disclosed by the clock timing extraction circuit with the diversity technique disclosed by the clock timing extraction circuit. The suggestion/motivation for doing so would have been to improve the quality of the data reception of a receiver (admitted prior art page 11 lines 2-4). Therefore, it would have been obvious to combine admitted prior art concerning code synchronization and admitted prior art concerning diversity to obtain the invention as specified in claims 1 and 12.

As per claims 2 and 13, admitted prior art discloses claims 1 and 12. Admitted prior art also discloses determining the absolute value (figure 1 and figure 2 block 1 page 3 lines 4-22); and selecting which said output value has the largest value, thereby determining said diversity output value (figure 2 block 13 page 3 lines 4-22). Admitted prior art also discloses receiving signal comprises at least two diversity branches and at least two filters and processing the data in at least two diversity branches (figures 6 and 7 block 67 page 11 line 1 to page 12 line 20). Admitted prior art concerning code synchronization and admitted prior art concerning diversity are analogous art because they are from the same field of endeavor. At the time of the invention, it would have been obvious to a person of ordinary skill in the art to incorporate in the code

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synchronization disclosed by the clock timing extraction circuit with the diversity technique disclosed by the clock timing extraction circuit. The suggestion/motivation for doing so would have been to improve the quality of the data reception of a receiver (admitted prior art page 11 lines 2-4). Therefore, it would have been obvious to combine admitted prior art concerning code synchronization and admitted prior art concerning diversity to obtain the invention as specified in claims 2 and 13.

As per claims 3 and 14, admitted prior art discloses claims 1 and 12. Admitted prior art also discloses determining the absolute value (figure 1 and figure 2 block 1 page 3 lines 4-22); and combining output values thereby producing said diversity output value (figure 7 block 89 page 11 line 1 to page 12 line 20). Admitted prior art also discloses receiving signal comprises at least two diversity branches and at least two filters and processing the data in at least two diversity branches (figures 6 and 7 block 67 page 11 line 1 to page 12 line 20). Admitted prior art concerning code synchronization and admitted prior art concerning diversity are analogous art because they are from the same field of endeavor. At the time of the invention, it would have been obvious to a person of ordinary skill in the art to incorporate in the code synchronization disclosed by the clock timing extraction circuit with the diversity technique disclosed by the clock timing extraction circuit. The suggestion/motivation for doing so would have been to improve the quality of the data reception of a receiver (admitted prior art page 11 lines 2-4). Therefore, it would have been obvious to combine admitted prior art concerning code synchronization and admitted prior art concerning diversity to obtain the invention as specified in claims 3 and 14.

As per claims 4 and 15, admitted prior art discloses claims 1 and 12. Admitted prior art also discloses determining the absolute value (figure 1 and figure 2 block 1 page 3 lines 4-22); determining an estimate of the relative power of each said diversity branch relative to the power of the remaining diversity branches (figures 6 and 7 page 11 line 1 to page 12 line 20); weighting for each said diversity branch by said relative power of said diversity branch thereby forming a weighted filter output for each said diversity branch (figure 7 block 89 page 11 line 1 to page 12 line 20); and combining said weighted filter output values thereby producing a diversity output value (figure 7 block 89 page 11 line 1 to page 12 line 20). Admitted prior art also discloses receiving signal comprises at least two diversity branches and at least two filters and processing the data in at least two diversity branches (figures 6 and 7 block 67 page 11 line 1 to page 12 line 20). Admitted prior art concerning code synchronization and admitted prior art concerning diversity are analogous art because they are from the same field of endeavor. At the time of the invention, it would have been obvious to a person of ordinary skill in the art to incorporate in the code synchronization disclosed by the clock timing extraction circuit with the diversity technique disclosed by the clock timing extraction circuit. The suggestion/motivation for doing so would have been to improve the quality of the data reception of a receiver (admitted prior art page 11 lines 2-4). Therefore, it would have been obvious to combine admitted prior art concerning code synchronization and admitted prior art concerning diversity to obtain the invention as specified in claims 4 and 15.

As per claims 5 and 16, admitted prior art discloses claims 1 and 12. Admitted prior art also discloses determining the relative instantaneous parallel channel estimate (figures 3 and 4 page 4 line 21 to page 7 line 14 to page 8 line 9); determining the complex conjugate of each said relative instantaneous channel estimate (figures 3 and 4 page 8 line 9); weighting each said filter output by the complex conjugate of said relative instantaneous channel estimate thereby producing a co-phased and weighted filter output (figures 3 and 4 page 8 line 9); combining each filter output thereby producing a weighted output magnitude (figures 3 and 4 page 8 line 9); and determining the real part of said co-phased, output magnitude, thereby producing an output value (figures 3 and 4 page 8 line 9). Admitted prior art also discloses receiving signal comprises at least two diversity branches and at least two filters and processing the data in at least two diversity branches (figures 6 and 7 block 67 page 11 line 1 to page 12 line 20). Admitted prior art concerning code synchronization and admitted prior art concerning diversity are analogous art because they are from the same field of endeavor. At the time of the invention, it would have been obvious to a person of ordinary skill in the art to incorporate in the code synchronization disclosed by the clock timing extraction circuit with the diversity technique disclosed by the clock timing extraction circuit. The suggestion/motivation for doing so would have been to improve the quality of the data reception of a receiver (admitted prior art page 11 lines 2-4). Therefore, it would have been obvious to combine admitted prior art concerning code synchronization and admitted prior art concerning diversity to obtain the invention as specified in claims 5 and 16.

As per claims 6 and 17, admitted prior art discloses claims 1 and 12. Admitted prior art also discloses generating a plurality of partial code correlations y_k , each said partial code correlation being characterized by a time index k (page 8 line 11 to page 9 line 3); Fourier transforming said plurality of partial code correlations y_k thereby forming a plurality of Fourier transform vectors (page 9 lines 4-15); determining the absolute value of each element of each said Fourier transform vector (figure 1 and figure 2 block 1 page 3 lines 4-22); selecting vector element with largest absolute value, thereby forming a selection diversity Fourier transform vector $X(p)$ (figure 2 block 13 page 3 lines 4-22 and page 9 lines 4-15); averaging said selection diversity Fourier transform vector over at least one Fourier transform block thereby forming an averaged selection Fourier transform vector (figure 2 block 11 page 3 lines 4-22 and page 9 lines 4-15); and selecting the element of said averaged selection diversity Fourier transform vector with largest absolute value (figure 2 block 13 page 3 lines 4-22 and page 9 lines 4-15). Admitted prior art also discloses receiving signal comprises at least two diversity branches and at least two filters and processing the data in at least two diversity branches (figures 6 and 7 block 67 page 11 line 1 to page 12 line 20). Admitted prior art also discloses receiving signal comprises at least two diversity branches and at least two filters and processing the data in at least two diversity branches (figures 6 and 7 block 67 page 11 line 1 to page 12 line 20). Admitted prior art concerning code synchronization and admitted prior art concerning diversity are analogous art because they are from the same field of endeavor. At the time of the invention, it would have been obvious to a person of ordinary skill in the art to incorporate in the code

synchronization disclosed by the clock timing extraction circuit with the diversity technique disclosed by the clock timing extraction circuit. The suggestion/motivation for doing so would have been to improve the quality of the data reception of a receiver (admitted prior art page 11 lines 2-4). Therefore, it would have been obvious to combine admitted prior art concerning code synchronization and admitted prior art concerning diversity to obtain the invention as specified in claims 6 and 17.

As per claims 7 and 18, admitted prior art discloses claims 1 and 12. Admitted prior art also discloses generating a plurality of partial code correlations y_k , each said partial code correlation being characterized by a time index k (page 8 line 11 to page 9 line 3); Fourier transforming said plurality of partial code correlations y_k thereby forming a plurality of Fourier transform vectors (page 9 lines 4-15); determining the absolute value of each element (figure 1 and figure 2 block 1 page 3 lines 4-22); summing the absolute value, thereby forming a non-coherent combining (figure 1 and figure 2 block 1 page 3 lines 4-22; and page 5 line 16 to page 6 line 13); averaging said non-coherent combining forming an averaged non-coherent combining vector (figure 2 block 11 page 3 lines 4-22 and page 9 lines 4-15); and selecting the element of said averaged non-coherent combining diversity vector with largest absolute value (figure 2 block 13 page 3 lines 4-22 and page 9 lines 4-15). Admitted prior art also discloses receiving signal comprises at least two diversity branches and at least two filters and processing the data in at least two diversity branches (figures 6 and 7 block 67 page 11 line 1 to page 12 line 20). Admitted prior art also discloses receiving signal comprises at least two diversity branches and at least two filters and processing the data in at least two

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diversity branches (figures 6 and 7 block 67 page 11 line 1 to page 12 line 20). Admitted prior art concerning code synchronization and admitted prior art concerning diversity are analogous art because they are from the same field of endeavor. At the time of the invention, it would have been obvious to a person of ordinary skill in the art to incorporate in the code synchronization disclosed by the clock timing extraction circuit with the diversity technique disclosed by the clock timing extraction circuit. The suggestion/motivation for doing so would have been to improve the quality of the data reception of a receiver (admitted prior art page 11 lines 2-4). Therefore, it would have been obvious to combine admitted prior art concerning code synchronization and admitted prior art concerning diversity to obtain the invention as specified in claims 7 and 18.

As per claims 8 and 19, admitted prior art discloses claims 1 and 12. Admitted prior art also discloses determining relative power of each said diversity branch relative to other said diversity branches (figures 6 and 7 page 11 line 1 to page 12 line 20); generating a plurality of partial code correlations y_k , each said partial code correlation being characterized by a time index k (page 8 line 11 to page 9 line 3); Fourier transforming said plurality of partial code correlations y_k thereby forming a plurality of Fourier transform vectors (page 9 lines 4-15); determining the absolute value of each element (figure 1 and figure 2 block 1 page 3 lines 4-22); weighting the absolute value of each said element vector by the relative power of said diversity branch thereby forming weighted vector elements (figure 7 block 87 page 11 line 1 to page 12 line 20); summing the absolute value, thereby forming a non-coherent combining (figure 1 and

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figure 2 block 1 page 3 lines 4-22; and page 5 line 16 to page 6 line 13); averaging said non-coherent combining forming an averaged non-coherent combining vector (figure 2 block 11 page 3 lines 4-22 and page 9 lines 4-15); and selecting the element of said averaged non-coherent combining diversity vector with largest absolute value (figure 2 block 13 page 3 lines 4-22 and page 9 lines 4-15). Admitted prior art also discloses receiving signal comprises at least two diversity branches and at least two filters and processing the data in at least two diversity branches (figures 6 and 7 block 67 page 11 line 1 to page 12 line 20). Admitted prior art also discloses receiving signal comprises at least two diversity branches and at least two filters and processing the data in at least two diversity branches (figures 6 and 7 block 67 page 11 line 1 to page 12 line 20). Admitted prior art concerning code synchronization and admitted prior art concerning diversity are analogous art because they are from the same field of endeavor. At the time of the invention, it would have been obvious to a person of ordinary skill in the art to incorporate in the code synchronization disclosed by the clock timing extraction circuit with the diversity technique disclosed by the clock timing extraction circuit. The suggestion/motivation for doing so would have been to improve the quality of the data reception of a receiver (admitted prior art page 11 lines 2-4). Therefore, it would have been obvious to combine admitted prior art concerning code synchronization and admitted prior art concerning diversity to obtain the invention as specified in claims 8 and 19.

As per claims 9 and 20, admitted prior art discloses claims 1 and 12. Admitted prior art also discloses determining a plurality of partial code correlations y_k , each said

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partial code correlation y_k being characterized by a time index k (page 8 line 11 to page 9 line 3); determining the complex conjugate y^*k (page 9 line 16 to page 10 line 4); determining a differential detection output $X(k)$ according to $X(k)=y_k y^*(k-1)$ (page 9 line 16 to page 10 line 4); determining the absolute value (figure 1 and figure 2 block 1 page 3 lines 4-22); selecting the largest absolute value thereby forming a selection differential detection output (figure 2 block 13 page 3 lines 4-22); determining a plurality of selection detection outputs for a plurality of k index values (figure 2 block 11 page 3 lines 4-22); averaging said plurality of selection forming an average selection output (figure 2 block 11 page 3 lines 4-22 and page 9 lines 4-15); and determining the synchronization frequency from said averaged (page 8 lines 11-15). Admitted prior art also discloses receiving signal comprises at least two diversity branches and at least two filters and processing the data in at least two diversity branches (figures 6 and 7 block 67 page 11 line 1 to page 12 line 20). Admitted prior art concerning code synchronization and admitted prior art concerning diversity are analogous art because they are from the same field of endeavor. At the time of the invention, it would have been obvious to a person of ordinary skill in the art to incorporate in the code synchronization disclosed by the clock timing extraction circuit with the diversity technique disclosed by the clock timing extraction circuit. The suggestion/motivation for doing so would have been to improve the quality of the data reception of a receiver (admitted prior art page 11 lines 2-4). Therefore, it would have been obvious to combine admitted prior art concerning code synchronization and admitted prior art concerning diversity to obtain the invention as specified in claims 9 and 20.

As per claims 10 and 21, admitted prior art discloses claims 1 and 12. Admitted prior art also discloses determining a plurality of partial code correlations y_k , each said partial code correlation y_k being characterized by a time index k (page 8 line 11 to page 9 line 3); determining the complex conjugate y_k^* of partial code correlation y_k (page 9 line 16 to page 10 line 4); determining a differential detection output $X(k)$ according to $X(k)=y_k y_{(k-1)}^*$ (page 9 line 16 to page 10 line 4); combining said differential detection output values thereby producing coherent diversity differential detection output value (figure 7 block 89 page 11 line 1 to page 12 line 20); determining a coherent detection outputs for a plurality of k index values (page 7 line 14 to page 8 line 9); averaging said plurality of coherent differential detection outputs thereby forming an average coherent detection output (figure 2 block 11 page 3 lines 4-22 and page 9 lines 4-15); and determining the synchronization frequency from said averaged coherent differential detection output (page 8 lines 11-15). Admitted prior art also discloses receiving signal comprises at least two diversity branches and at least two filters and processing the data in at least two diversity branches (figures 6 and 7 block 67 page 11 line 1 to page 12 line 20). Admitted prior art concerning code synchronization and admitted prior art concerning diversity are analogous art because they are from the same field of endeavor. At the time of the invention, it would have been obvious to a person of ordinary skill in the art to incorporate in the code synchronization disclosed by the clock timing extraction circuit with the diversity technique disclosed by the clock timing extraction circuit. The suggestion/motivation for doing so would have been to improve the quality of the data reception of a receiver (admitted prior art page 11 lines 2-4).

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Therefore, it would have been obvious to combine admitted prior art concerning code synchronization and admitted prior art concerning diversity to obtain the invention as specified in claims 10 and 21.

As per claims 11 and 22, admitted prior art discloses claims 1 and 12. Admitted prior art also discloses determining a plurality of partial code correlations y_k , each said partial code correlation y_k being characterized by a time index k (page 8 line 11 to page 9 line 3); determining the complex conjugate y_k^* of partial code correlation y_k (page 9 line 16 to page 10 line 4); determining an estimate of the relative power of each said diversity branch relative to the power of the remaining diversity branches (figures 6 and 7 page 11 line 1 to page 12 line 20); determining a differential detection output $X(k)$ according to $X(k) = y_k y_{(k-1)}^*$ (page 9 line 16 to page 10 line 4); weighting the absolute value of each said element vector by the relative power of said diversity branch thereby forming weighted vector elements (figure 7 block 87 page 11 line 1 to page 12 line 20); combining said differential detection output values thereby producing coherent diversity differential detection output value (figure 7 block 89 page 11 line 1 to page 12 line 20); determining a coherent detection outputs for a plurality of k index values (page 7 line 14 to page 8 line 9); averaging said plurality of coherent differential detection outputs thereby forming an average coherent detection output (figure 2 block 11 page 3 lines 4-22 and page 9 lines 4-15); and determining the synchronization frequency from said averaged coherent differential detection output (page 8 lines 11-15). Admitted prior art also discloses receiving signal comprises at least two diversity branches and at least two filters and processing the data in at least two diversity branches (figures 6 and 7

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block 67 page 11 line 1 to page 12 line 20). Admitted prior art concerning code synchronization and admitted prior art concerning diversity are analogous art because they are from the same field of endeavor. At the time of the invention, it would have been obvious to a person of ordinary skill in the art to incorporate in the code synchronization disclosed by the clock timing extraction circuit with the diversity technique disclosed by the clock timing extraction circuit. The suggestion/motivation for doing so would have been to improve the quality of the data reception of a receiver (admitted prior art page 11 lines 2-4). Therefore, it would have been obvious to combine admitted prior art concerning code synchronization and admitted prior art concerning diversity to obtain the invention as specified in claims 11 and 22.

As per claim 23, admitted prior art discloses method for synchronizing a mobile terminal to a wireless WCDMA network comprising the steps of receiving a signal comprising a primary synchronization code, a secondary synchronization code and a gold code, broadcast by a base-station with a mobile terminal (page 4 lines 12-20 and page 28 line15 to page 29 line 8); processing said received signal to determine the primary synchronization code (page 4 lines 12-20 and page 28 line15 to page 29 line 8); determining the slot boundary based upon the determination of said primary synchronization code (page 4 lines 12-20 and page 28 line15 to page 29 line 8); processing said received signal to determine the secondary synchronization code based upon said slot boundary (page 4 lines 12-20 and page 28 line15 to page 29 line 8); determining the code group based upon the determination of said secondary synchronization code (page 4 lines 12-20 and page 28 line15 to page 29 line 8);

processing said received signal to determine the gold code based upon said slot boundary and said code group (page 4 lines 12-20 and page 28 line 15 to page 29 line 8); and synchronizing the local oscillator of said mobile terminal based upon said gold code (page 8 lines 11-15). Admitted prior art also discloses receiving signal comprises at least two diversity branches and at least two filters and processing the data in at least two diversity branches (figures 6 and 7 block 67 page 11 line 1 to page 12 line 20). Admitted prior art concerning code synchronization and admitted prior art concerning diversity are analogous art because they are from the same field of endeavor. At the time of the invention, it would have been obvious to a person of ordinary skill in the art to incorporate in the code synchronization disclosed by the clock timing extraction circuit with the diversity technique disclosed by the clock timing extraction circuit. The suggestion/motivation for doing so would have been to improve the quality of the data reception of a receiver (admitted prior art page 11 lines 2-4). Therefore, it would have been obvious to combine admitted prior art concerning code synchronization and admitted prior art concerning diversity to obtain the invention as specified in claim 23.

As per claim 24, admitted prior art discloses a method for synchronizing a mobile terminal to a wireless WCDMA network comprising receiving a signal comprising a primary synchronization code, a secondary synchronization code and a gold code, broadcast by a base-station with a mobile terminal, wherein said mobile terminal comprises a 256 symbol primary synchronization code matched filter (page 4 lines 12-20 and page 28 line 15 to page 29 line 8); a primary and secondary synchronization code matched correlator; a Gold code matched correlator (page 4 lines 12-20 and page

28 line15 to page 29 line 8); and least one Fourier transform non-coherent combining frequency synchronization receiver (page 9 lines 4-15); processing said received signal using said non-coherent receiver (figure 2 block 13 page 3 lines 4-22); averaging non-coherent output values over N slot repetitions (figure 2 block 11 page 3 lines 4-22 and page 9 lines 4-15); selecting the maximum value of step c to determine said primary synchronization code (figure 2 block 13 page 3 lines 4-22); determining the slot boundary from said primary synchronization code (page 4 lines 12-20 and page 28 line15 to page 29 line 8); correlating said received signal against each of the 64 secondary synchronization codes and the 15 circular rotations of each said secondary synchronization code using said coherent combining diversity receiver, wherein the channel estimate is provided by the primary synchronization code correlator output, thereby determining 960 (64X15) coherent combining diversity output values (page 4 lines 12-20 and page 28 line15 to page 29 line 8); determining the secondary synchronization code based upon the maximum value of said 960 coherent combining diversity output values (page 4 lines 12-20 and page 28 line15 to page 29 line 8); correlating for R consecutive samples said received signal against each of the 8 possible gold codes to determine 8 Gold code diversity outputs using said non-coherent combining diversity receiver (page 4 lines 12-20 and page 28 line15 to page 29 line 8); generating a pluralities of said 8 Gold code diversity outputs (page 4 lines 12-20 and page 28 line15 to page 29 line 8); determining said gold code based upon said pluralities of Gold code non-coherent output values; synchronizing the local oscillator of said mobile terminal with said Fourier transform non-coherent combining diversity

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frequency synchronization receiver based upon said gold code (page 4 lines 12-20 and page 28 line 15 to page 29 line 8). Admitted prior art also discloses receiving signal comprises at least two diversity branches and at least two filters and processing the data in at least two diversity branches (figures 6 and 7 block 67 page 11 line 1 to page 12 line 20). Admitted prior art concerning code synchronization and admitted prior art concerning diversity are analogous art because they are from the same field of endeavor. At the time of the invention, it would have been obvious to a person of ordinary skill in the art to incorporate in the code synchronization disclosed by the clock timing extraction circuit with the diversity technique disclosed by the clock timing extraction circuit. The suggestion/motivation for doing so would have been to improve the quality of the data reception of a receiver (admitted prior art page 11 lines 2-4). Therefore, it would have been obvious to combine admitted prior art concerning code synchronization and admitted prior art concerning diversity to obtain the invention as specified in claim 24.

Conclusion

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Llang (US 20030026348 A1) discloses the use of antenna diversity in CDMA mobile terminals (paragraph [0176]). Scherzer (US 6347234 B1) discloses a diversity practical space-time radio method for CDMA communication capacity enhancement. Hosur (US 6356605 B1) discloses frame synchronization in space time block coded transmit antenna diversity for WCDMA. Fukawa (US 5790588 A) discloses a spread spectrum transmitter and receiver employing composite

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spreading codes. Oates (US 20030076875 A1) discloses diversity CDMA system.


Prasad ("An Overview of CDMA Evolution toward Wideband CDMA", 1998, IEEE, pp 1-29) discloses the use of receiver diversity in a mobile terminal for WCDMA (page 23).

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Juan A. Torres whose telephone number is (571) 272-3119. The examiner can normally be reached on Monday-Friday 9:00 AM - 5:00 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Mohammad H. Ghayour can be reached on (571) 272-3021. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Juan Alberto Torres
11-16-2005


KEVIN BURD
PRIMARY EXAMINER